



## We are recruiting!

### Junior HW-FPGA Design Engineer (Systems Department)

#### QUALIFICATIONS:

- ✓ Telecommunication or Electronic Engineer.

#### SKILLS:

- ✓ Knowledge of C/C++ and Python programming languages.
- ✓ Good knowledge of digital design.
- ✓ FPGA design tools and language (VHDL/Verilog).
- ✓ PCB design skills: schematics, layout and simulation.
- ✓ Power and signal integrity.
- ✓ Embedded systems architecture and main HW components, including FPGA devices.
- ✓ Communication interfaces: I2C, SPI, LVDS, USB, Ethernet, etc.

#### PREVIOUS EXPERIENCE:

- ✓ Desirable experience in PCB and FPGA design.

#### LANGUAGES:

- ✓ High level of English, both spoken and written. (Similar to C1 level.)

#### RESPONSIBILITIES:

- ✓ Design and simulation of digital blocks to be implemented in the FPGA.
- ✓ Debug of HW/FPGA design with proper instrumentation (oscilloscope, etc.).
- ✓ HW/PCB design (schematics and layout) and simulations.

For job applications, visit:  
<https://spexternal-teledyne.icims.com>



**TELEDYNE ANAFOCUS**  
Everywhereyoulook™

Part of the Teledyne Imaging Group